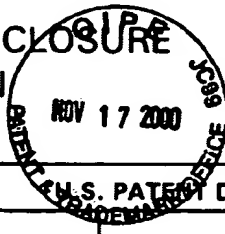


[illegible]

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INFORMATION DISCLOSURE CITATION

PTO-1449

ATTORNEY'S DKT NO.
040071-134APPLICATION NO.
09/580,632APPLICANT
Sven MATTISSON, et al.FILING DATE
May 30, 2000GROUP
2858

U.S. PATENT DOCUMENTS

| EXAMINER'S INITIALS | PATENT NO. | DATE | NAME | CLASS | SUBCLASS | FILING DATE |
|------------------------|------------|-----------|----------|-------|----------|-------------|
| CO | 6,049,233 | 4-11-2000 | Shurboff | | | |
| CO | 5,631,582 | 5-20-1997 | Fujikawa | | | |
| CO | 5,103,191 | 4-7-1992 | Werker | | | |
| CO | 4,818,950 | 4-4-1989 | Ranger | | | |
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FOREIGN PATENT DOCUMENTS

| EXAMINER'S INITIALS | PATENT NO. | DATE | COUNTRY | CLASS | SUBCLASS | Translation | |
|------------------------|------------|------|---------|-------|----------|-------------|----|
| | | | | | | Yes | No |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| | |
|----|---|
| CO | D. P. Turner, "Phase-Locked Loop Phase Adjustment," IBM Technical Disclosure Bulletin, U.S., IBM Corp., New York, Vol. 15, No. 7, December 1972, pages 2080-2081. |
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EXAMINER

Curtis Adon

DATE CONSIDERED

2/7/03

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